

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): En-Hsing Chen, et al.

Title: NAND MEMORY ARRAY INCORPORATING CAPACITANCE  
BOOSTING OF CHANNEL REGIONS IN UNSELECTED MEMORY  
CELLS AND METHOD FOR OPERATION OF SAME

Application No.: 10/729,831

Filed: December 5, 2003

Examiner: Pham, Ly D.

Group Art Unit: 2827

Atty. Docket No.: 023-0028

Confirmation No.: 8528

May 3, 2006

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450**RESPONSE TO RESTRICTION REQUIREMENT**

This paper is responsive to the Non-Final Office Action mailed on April 3, 2006 having a shortened statutory period for response set to expire May 3, 2006.

*Election of Invention*

In response to the Examiner's restriction requirement, the undersigned hereby elects Group II, pending claims 15-25, 50-60, 89-99, and 126-136.

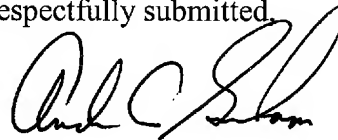
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Date

Respectfully submitted,



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